

IN THE SPECIFICATION

Please amend the paragraph at page 27, lines 15-19 to read as follows:

Then, the NMOS transistor N1 is turned on as in any above-described embodiment.  
An L-level voltage is thus applied to the gate of the NMOS transistor N2 via the second pad  
52. The NMOS transistor N2 is turned on off, and the thyristor circuit 90 is turned off.